Chip design & industrialisation

Chip design, NEXT, September 29th















Chip design & industrialisation

A chip development takes time, cost lots of money, small chance to success...

Everything is already there...

SystematIC.... who?? Time, money, success















Systematic facilitates innovation







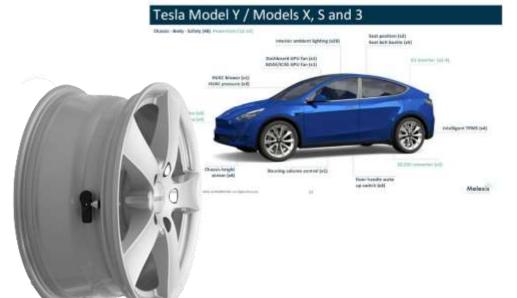


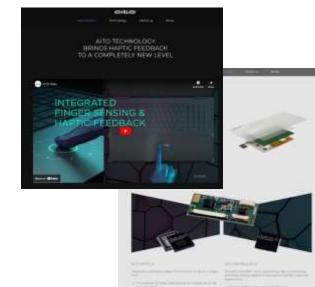






























Systematic company

- Integrated Circuits: design, IP and products
- Innovative design sensor and power chips
- Industrial, consumer, automotive markets
- 25 years in Delft
- >20 years in Asia
- >5 years products

Systematic provides technology for *smarter*, *smaller* and energy *efficient* systems



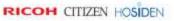






RENESAS DENSO



















Innovation...our mission



Innovation:

bring technology successfully to the market

Innovation in integration:

EV & solar, haptic, biometric, class D audio, AI, energy harvesting, security.... SystematIC projects 2023

Innovation through cooperation: Foundry, Test & Assembly, M&S IDM/OEM Complementary tech and new tech















Importance Delft

- World famous TU: engineers, education
- SystematIC: design methodology
- System@IC: SoC, IP

Time, money, success









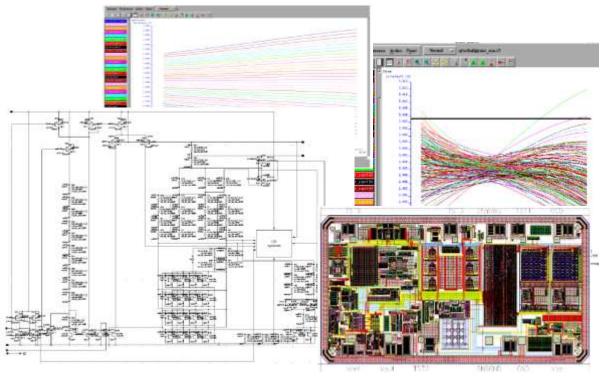








Day to day work...



Chip architecture...design...layout... Educated and experienced team, procedures, reviews, advanced tooling, mature IC processes













.... industrialisation

Prototype Pre-production Production

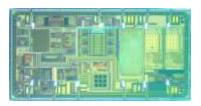
Idea Feasibility IC Specification IC design & Evaluation Waferfab Wafertest Assembly Final test Qualification Supply chain managment

SystematIC design

Production partners

SystematIC

Deliverable to Customer



Time, money, succes Time, money, succes















Technology Trends in IC's

Big Trends... AI, wireless, Smart, EV, Digitisation

Motivation to integrate on a chip:

- Technological/Physical
- Strategic (IP protection)
- Commercial

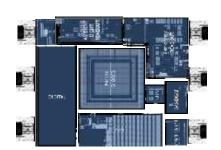


IC trends in our domain:

- Features (SoC), DFT, diagnosticslarger chip
- Less external componentslarger chip
- Cost pressure ...smaller chip

Technology

- CMOS:180...130...40...12 nm: mature processes
- HV, sensors, More than Moore, IP: reuse
- Low volume and local test/assembly options



Time, money, success











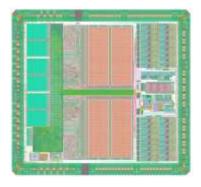


Example: System@IC





- SoC integrated prototype for startup
- Sensor, modelling, AFE, ADC
- Digital: team, tools, ARM
- Succesfull first silicon!











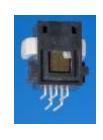




Example: Isolated current sensor











- Current sensors industrial, automotive & smart grid
- Applications: inverters, OCP, BMS
- Reduce BOM, smaller and smaller devices, features and functions













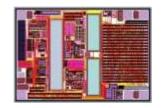
Example: PSALS

- Ambient light and proximity sensors
- Ambient light response similar to the human eye
- Proximity detection IR led based
- Low cost PS-ALS sensors use integrated diodes and IR light

Trend smaller and smaller: cheaper²



















IC: Bridge to product innovation

- Make the possible possible in integration (laws of physics do apply)
- Optimize time and cost, reduce risks in custom IC's (business case?)
- Cooperation and supply chain partners in NL-EU

IC technology provides a bridge and facilitates product innovation







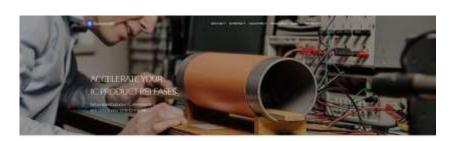








Thank you for your attention!





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